



IFW

1374.42869TRN

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: CHAKIHARA, et al.  
Serial No.: 10/629,733  
Filed: July 30, 2003  
For: A SEMICONDUCTOR MEMORY DEVICE AND A METHOD OF  
MANUFACTURING THE SAME, A METHOD OF  
MANUFACTURING A VERTICAL MISFET AND A VERTICAL  
MISFET, AND A METHOD OF MANUFACTURING A  
SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR  
DEVICE  
Group: 2826  
Examiner: F. ABRAHAM

**SECOND SUPPLEMENTAL AMENDMENT**

**MS: AMENDMENTS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 16, 2005

Sir:

In supplement to the Amendments filed on July 29, 2005 and May 31, 2005,  
please further amend the above-identified application as listed below and as set forth  
on the following pages:

**Amendments to the Claims; and**

**Remarks are included following the Amendments.**